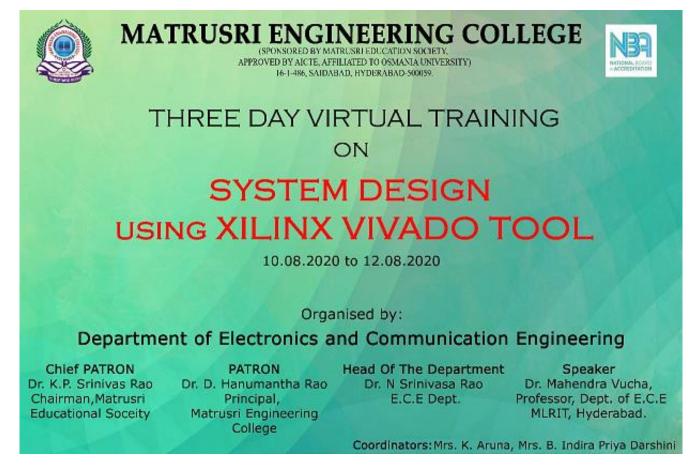
Dear Students/Faculty,

Greetings from the Department of Electronics and Communication Engineering, Matrusri Engineering college, Saidabad, Hyderabad.

We are glad to inform you that the Department of ECE, MECS is organizing a THREE DAY VIRTUAL TRAINING ON SYSTEM DESIGN USING XILINX VIVADO TOOL from 10/08/2020 to 12/08/2020.



To register for Webinar:

https://docs.google.com/forms/d/e/1FAIpQLSdfNNJxWi2d3Pf0fcgHy28hop16YNqiXjhxNuipcPXyP9uHrA/viewform ?usp=pp_url

This training facilitates the students for carrying out Mini & Major Projects using Xilinx Vivado tool. The Focused Topics of Training: Installation of Software Xilinx Vivado tool, Basics of Verilog HDL & Combinational circuit design, Sequential circuit design, System design and use cases of system design using Xilinx Vivado followed by Assignments given during the session.

Link for joining the session: <u>https://meet.google.com/lookup/ayr7cmwv4r?authuser=0&hs=179</u>

Registration Fee: Free of Charge.

Intended Participants: Students and Faculty Members Assignments given during the session must be submitted for getting E-certificate.

Attendance link will be shared at the end of each session.

Attending all the Sessions and submitting the final feedback by the Participants is mandatory.

Expecting an overwhelming response from your side.

Thank You,

Coordinators: Mrs. K.Aruna. Asst. Professor. ECE. Contact No: 9618235088 Mrs. B. Indira Priyadarshini, Asst. Professor, ECE, Contact No: 9441618939